INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known	
		Application Number	10/722818	
		Filing Date	1	
		First Named Inventor	Seong Hyun Kim	
		Art Unit	2814	
		Examiner Name	CJVACH	
Sheet	of	Attorney Docket Number	2013P135	

	U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.'	Document Number  Number - Kind Code <sup>1</sup> (if known)	Publication Date	Name of Patentee or Applicant of Citod Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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	FOREIGN PATENT DOCUMENTS						
Examiner Cite No.1	Foreign Patent Document			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
	Country Code <sup>3</sup> - Number <sup>a</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		To		
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STATEMEN	T BY APPLICANT	First Named Inventor	Seong Hyun Kim	
(use as many sheets as necessary)		Art Unit	7814	
		Examiner Name	BUACIT	
Sheet	of	Attorney Docket Number	2013P135	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
-1G	)	"Vertical p-type high-mobility heterojunction metal-oxide-semiconductor field-effect transistors", X. Chen, et al., 2001 American Institute of Physics		
16	-	Applied Physics Letters, "Conductance quantization in nanoscale vertical structure silicon field-effect transistors with a wrap gate", K. Nishiguchi, et al., 2000 American Institute of Physics		
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